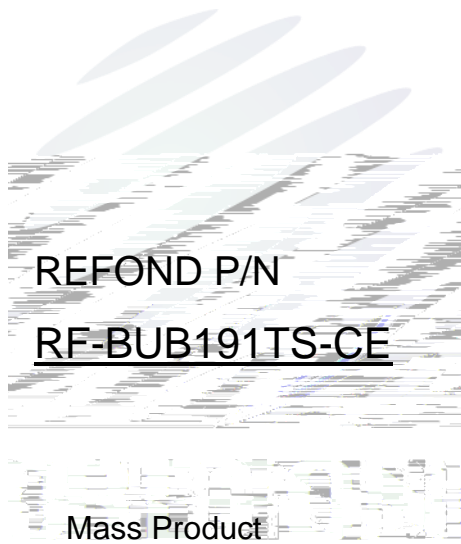


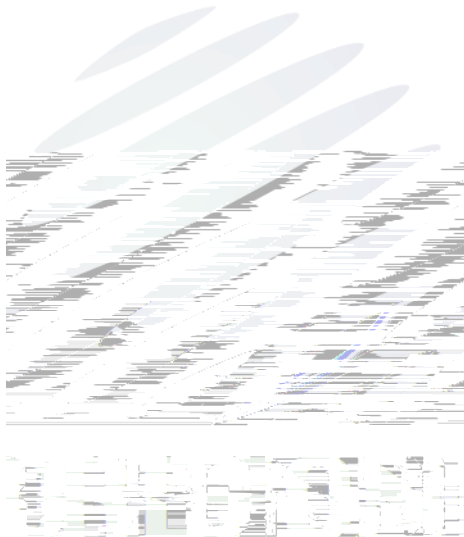
SPECIFICATION



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1.4 Package Dimension

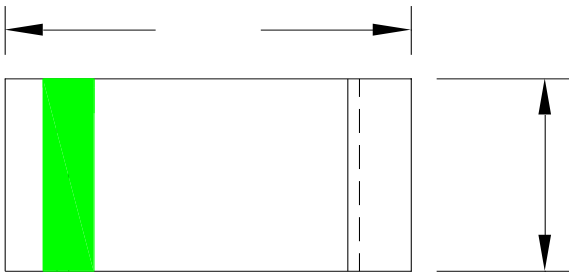


Fig.1-1 Top view

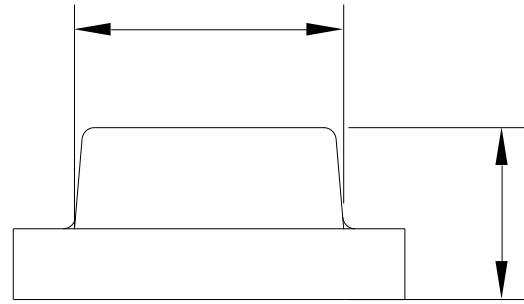


Fig.1-2 Side view

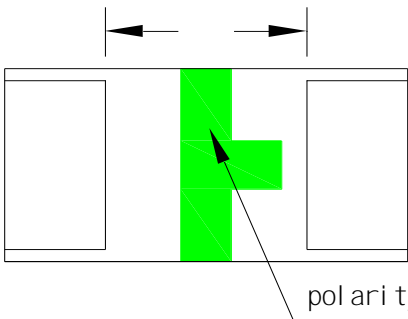


Fig.1-3 Bottom view

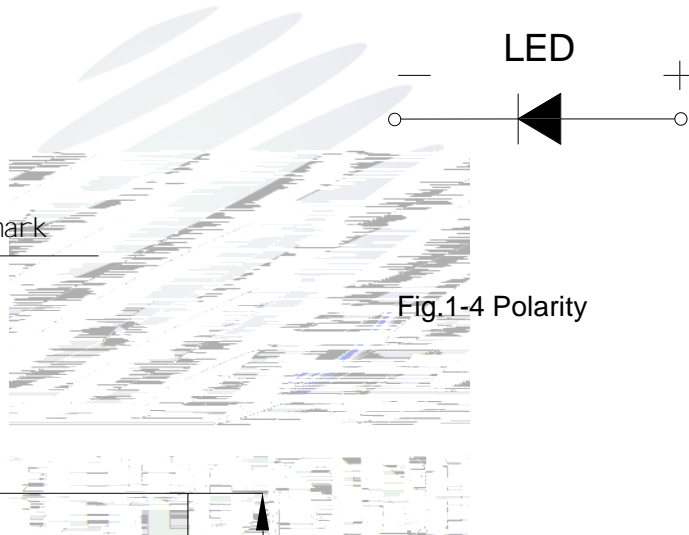


Fig.1-4 Polarity



Fig.1-5 Soldering patterns

Notes

All dimensions units are millimeters.

2. All dimensions tolerances are $\pm 0.2\text{mm}$ unless otherwise noted.



1.5 Product Parameters

Table 1-1 Electrical / Optical Characteristics at Ts=25°C

Item	Test Condition	Symbol	Value			Unit	
			Min. ()	Typ.	Max.		
Spectral Half Bandwidth	$I_F=20\text{mA}$	Δ	--	30	--	nm	
Forward Voltage	$I_F=20\text{mA}$	V_F	G1	2.8	--	2.9	V
			G2	2.9	--	3.0	V
			H1	3.0	--	3.1	V
			H2	3.1	--	3.2	V
			I1	3.2	--	3.3	V
			I2	3.3	--	3.4	V
Dominant Wavelength	$I_F=20\text{mA}$	λ_D	J1	3.4	--	3.5	V
			4F	465	--	466	nm
			1A	466	--	467	nm
			1B	467	--	468	nm
			1C	468	--	469	nm
			1D	469	--	470	nm
			1F	470	--	471	nm
			1G	471	--	472	nm
			1H	472	--	473	nm
			1I	473	--	474	nm
Luminous Intensity	$I_F=20\text{mA}$	I_v	5HC	474	--	475	nm
			1BL	120	--	140	mcd
			1BM	140	--	160	mcd
			1BW	160	--	180	mcd
			1BX	180	160		

Notes : $V_R=5V$ For test conditions. $V_R=5V$

Table 1-2 Absolute Maximum Ratings at Ts=25°C

Parameter	Symbol	Rating	Units
Power Dissipation	P_d	70	mW
Forward Current	I_F	20	mA
Peak Forward Current Of Pulse	I_{FP}	60	mA
Reverse Voltage	V_r	5	V
Electrostatic Discharge (HBM)	E_{SD}	1000	V
Operating Temperature	T_{opr}	-40 ~ +85	
Storage Temperature	T_{stg}	-40 ~ +85	
Junction Temperature	T_j	95	

Notes

- 1/10 Duty cycle, 0.1ms pulse width.
- The above forward voltage measurement allowance tolerance is $\pm 0.1V$.
- The above dominant wavelength measurement allowance tolerance is $\pm 2nm$.
- The above luminous intensity measurement allowance tolerance $\pm 10\%$.
- Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
- All measurements were made under the standardized environment of Refond.
- When the LEDs are in operation the maximum current should be decided after measuring the package temperature, junction temperature should not exceed the maximum rate



1.6 Typical Optical Characteristics Curves

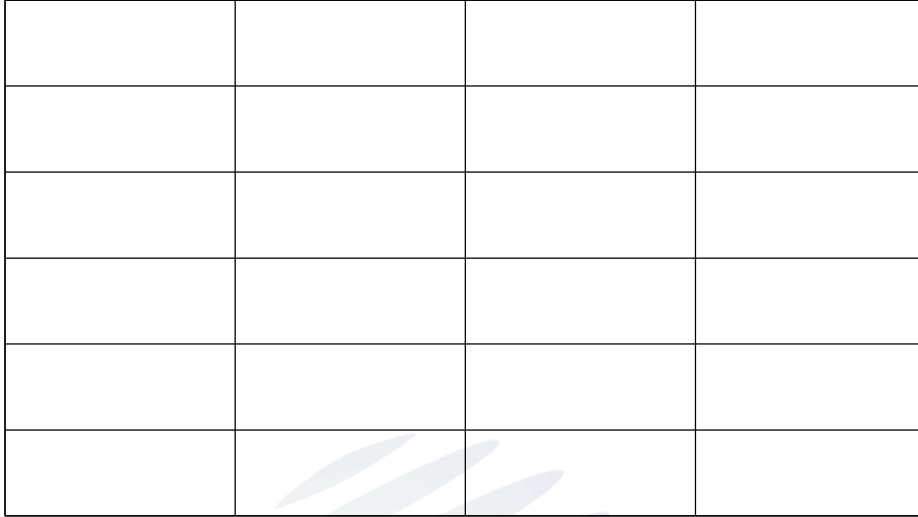


Fig 1-6 Forward Voltage Vs Forward Current

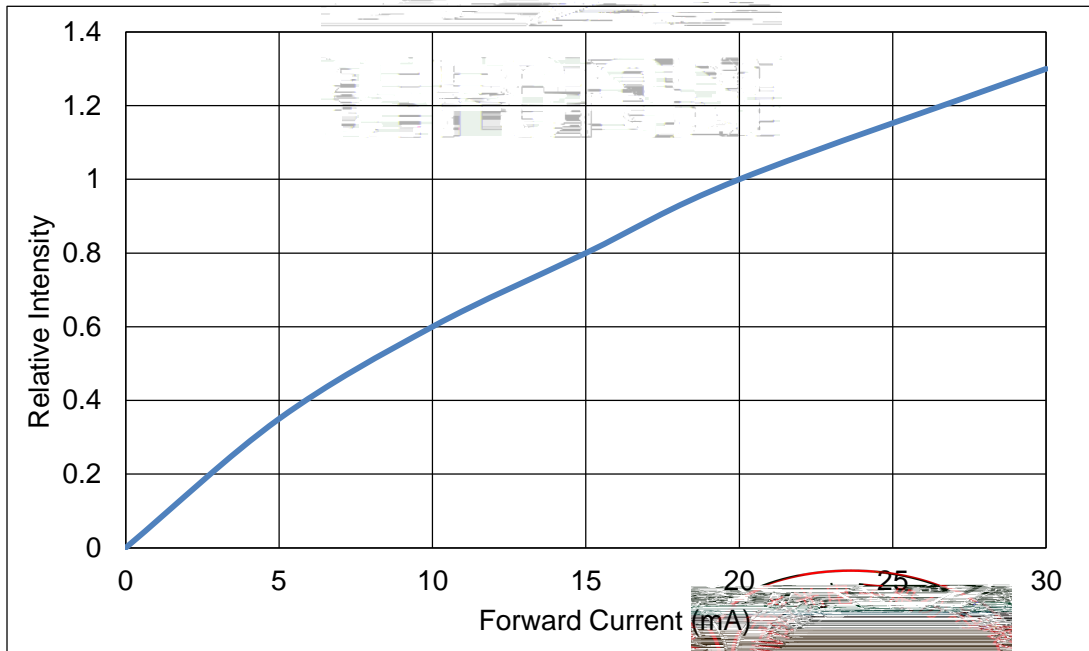


Fig 1-7 Forward Current Vs Relative Intensity

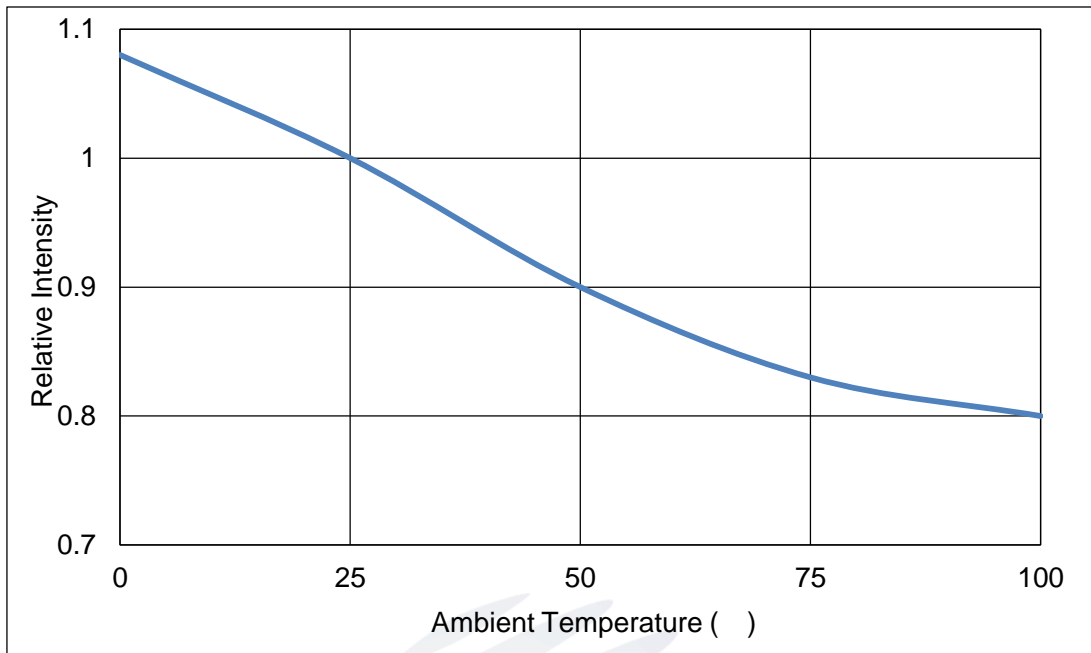


Fig 1-8 Pin Temperature Vs Relative Intensity

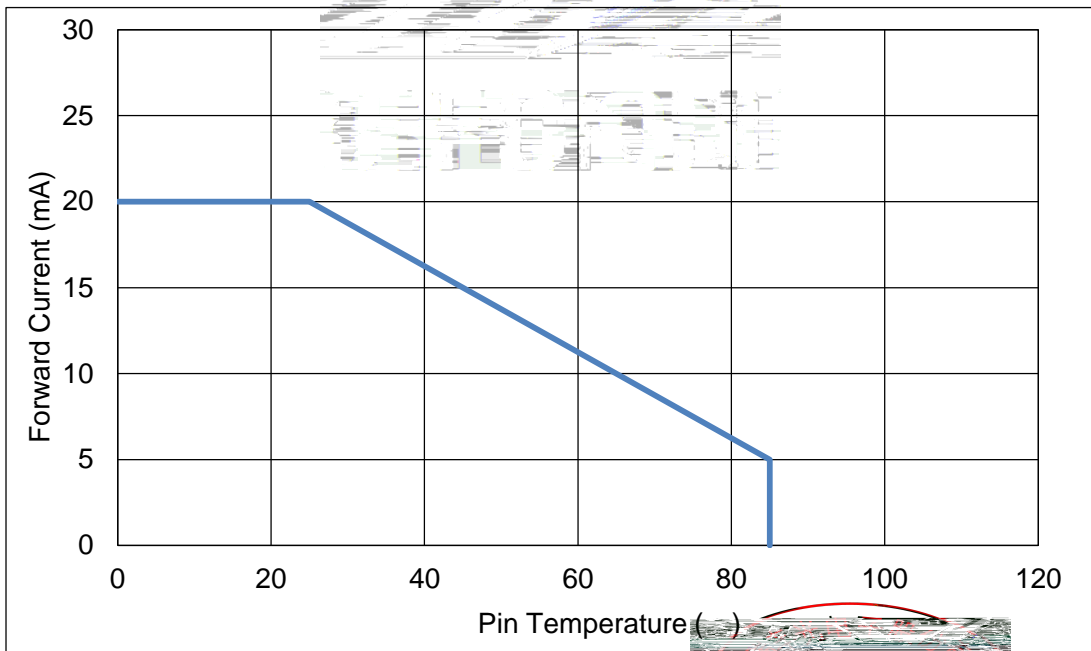


Fig 1-9 Pin Temperature Vs Forward Current

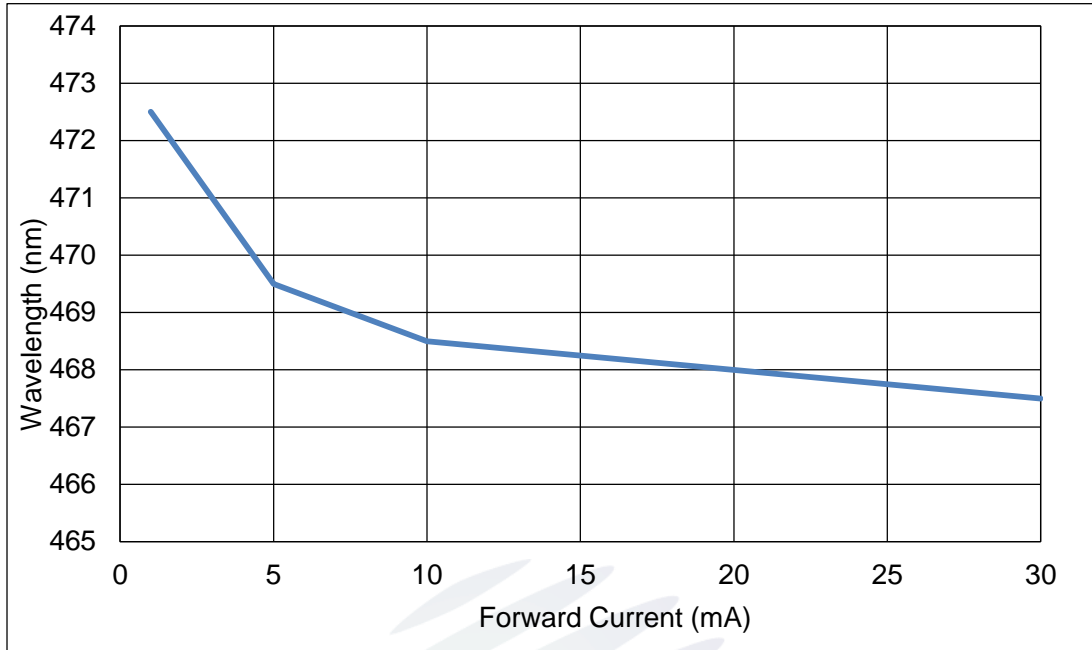


Fig 1-10 Forward Current Vs Dominate Wavelength (Ta=25)

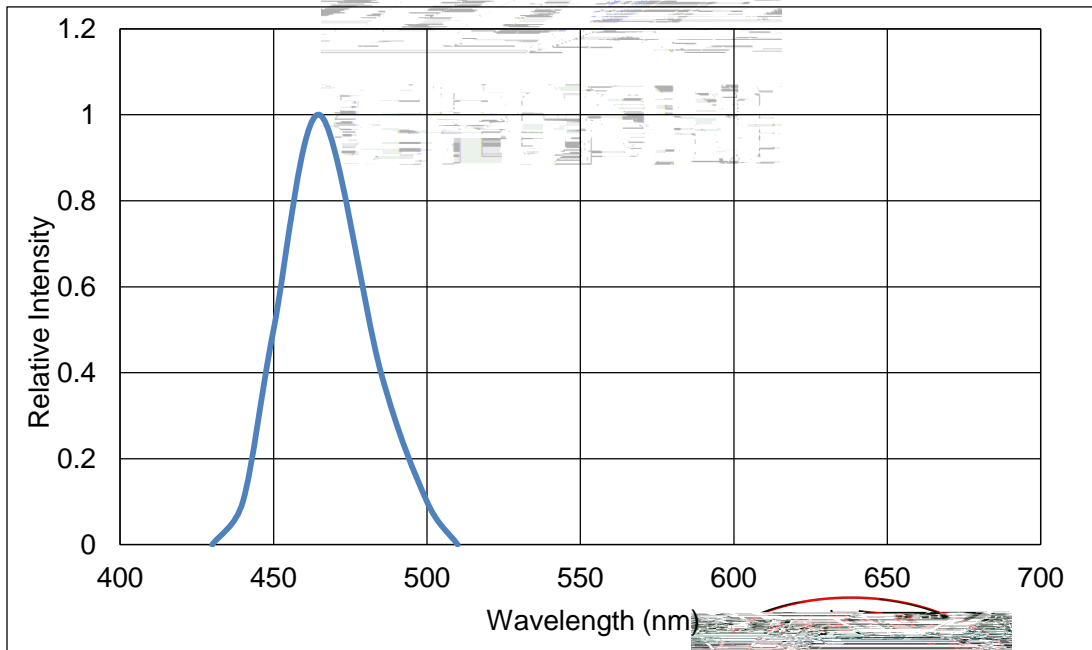


Fig 1-11 Relative Intensity Vs Wavelength (Ta=25)

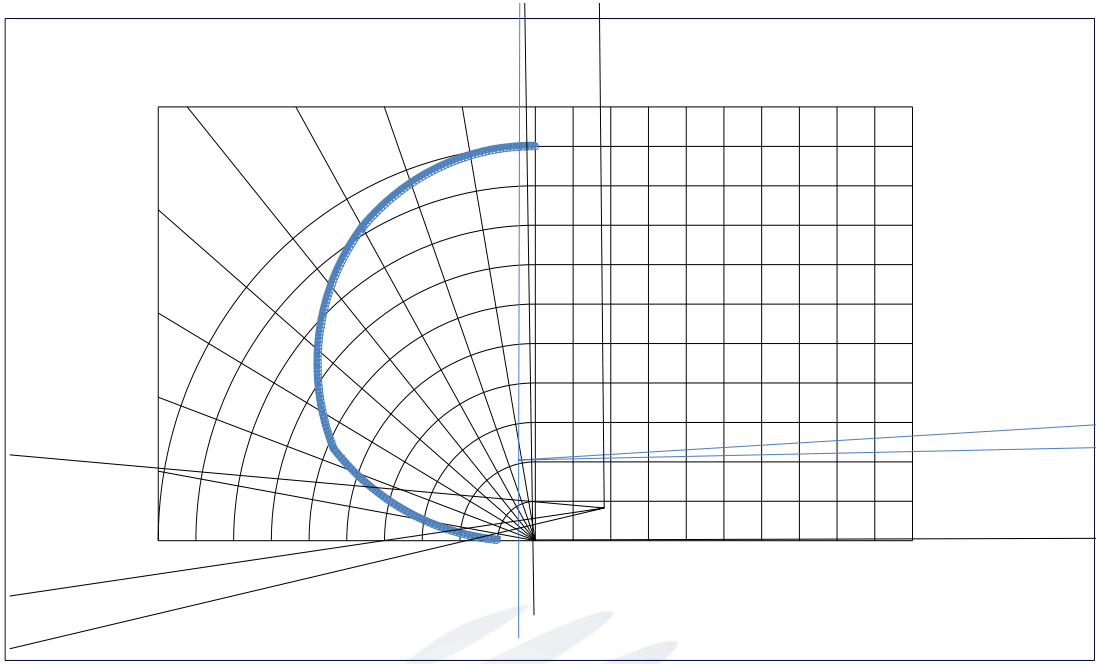
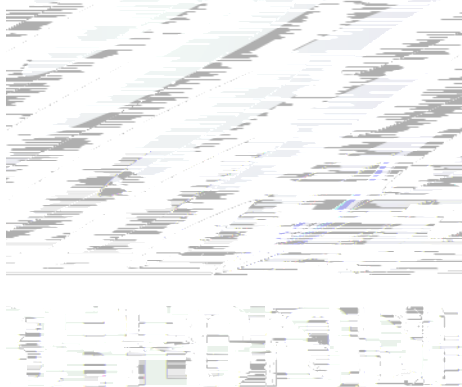


Fig 1-12 Diagram characteristics of radiation



2. Packaging

2.1 Packaging Specification

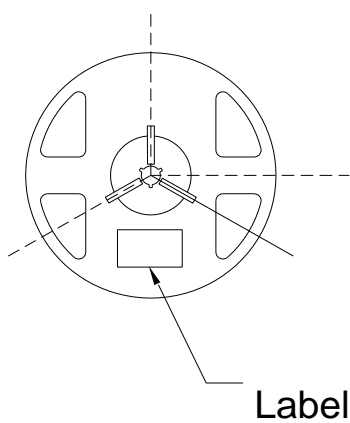
Package:4000pcs/reel. 4000pcs

2.1.1 Carrier Tape Dimension



Fig.2-1 Carrier Tape Dimension

2.1.2 Reel Dimension



D

Fig.2-2 Reel Dimension

Table 2-1 Dimension

A	8.0 0.1mm
B	178 1mm
C	60 1mm
D	13.0 0.5mm

Notes

The tolerances unless mentioned $\pm 0.1\text{mm}$. Unit : mm



2.1.3 Label Form Specification

Table 2-2 Parameter

PART NO.	Part Number
SPEC NO.	Spec Number
LOT NO.	Lot Number
BIN CODE	Bin Code
	Luminous flux
XY	Chromaticity Bin
V _F	Forward Voltage
WLD	Wavelength
QTY	Packing Quantity
DATE	Made Date

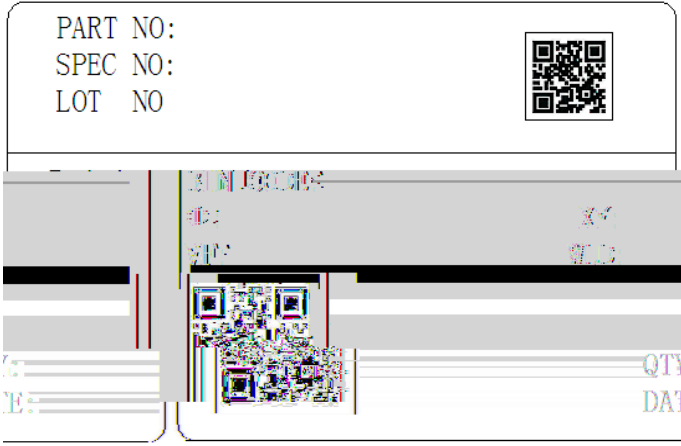


Fig. 2-3 Label Form Specification

2.2 Moisture Resistant Packing

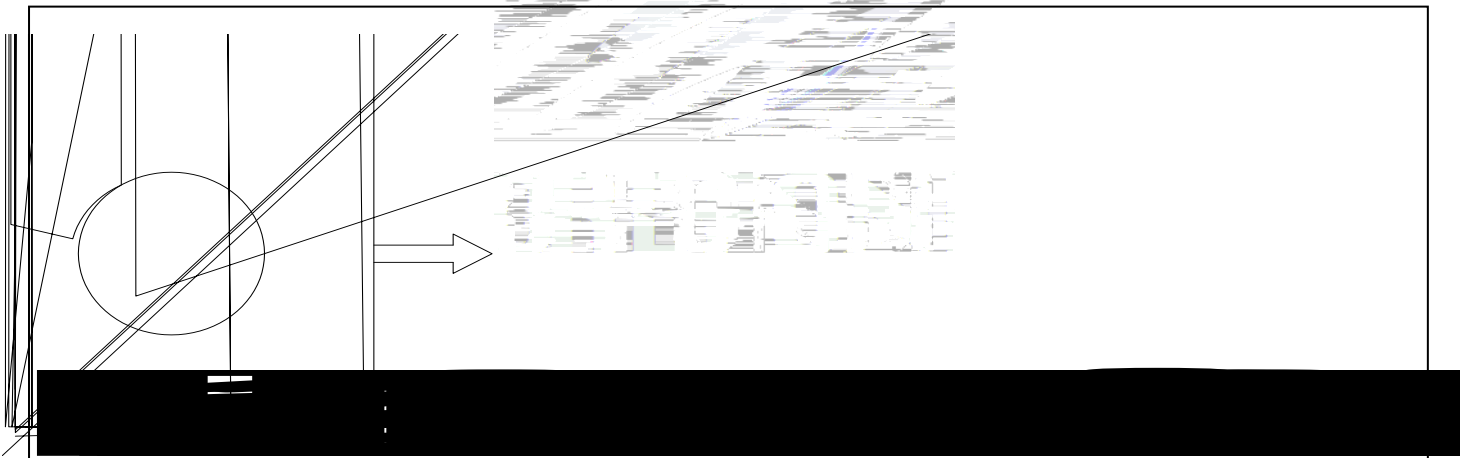


Fig.2-4 Moisture Resistant Packing



2.3 Cardboard Box

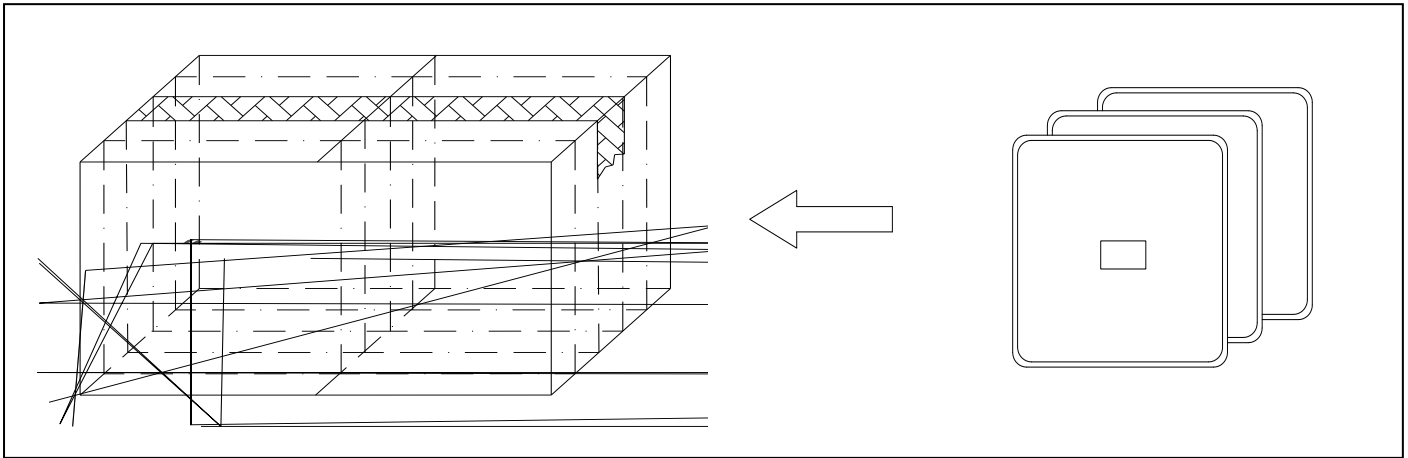


Fig.2-5 Cardboard Box

2.4 Reliability Test Items And Conditions

Table 2-3 Reliability Test Items And Conditions

Test Items	Ref.Standard	Test Condition	Time	Quantity	Ac/Re /
Reflow	JESD22-B106	Temp:260 max T=10 sec	2 times	22Pcs.	0/1
Temperature Cycle	JESD22-A104	100 30 min 5 min -40 30 min	100 cycles	22Pcs.	0/1
Thermal Shock	JESD22-A106	-40 15min 100 15min	300 cycles	22Pcs.	0/1
High Temperature Storage	JESD22-A103	Temp:100	1000 hrs.	22Pcs.	0/1
Low Temperature Storage	JESD22-A119	Temp:-40	1000 hrs.	22Pcs.	0/1
Life Test	JESD22-A108	T _a =25 I _F =20mA	1000 hrs.	22Pcs.	0/1



2.5 Criteria For Judging Damage

Table 2-4 Criteria For Judging Damage

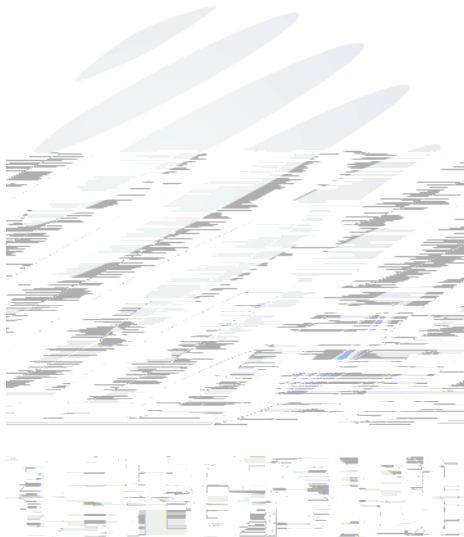
Test Items	Symbol	Test Condition	Criteria For Judgement	
			Min.	Max.
Forward Voltage	V_F	$I_F=20\text{mA}$	-	U.S.L*)x1.1
Reverse Current	I_R	$V_R= 5\text{V}$	-	U.S.L*)x2.0
Luminous Flux		$I_F=20\text{mA}$	L.S.L*)x0.7	-

Notes

1.U.S.L: Upper standard level

L.S.L: Lower standard level

2.The above reliability tests is based on the verification of a single/strip LED1 367.15 345.65 Tm[LE 343 382)BTef*463.



Notes

(1)Reflow soldering should not be done more than twice. If more than 24 hours between the two solderings , LED will be damaged.

(2)Whensoldering , do not put stress on the LEDs during heating.

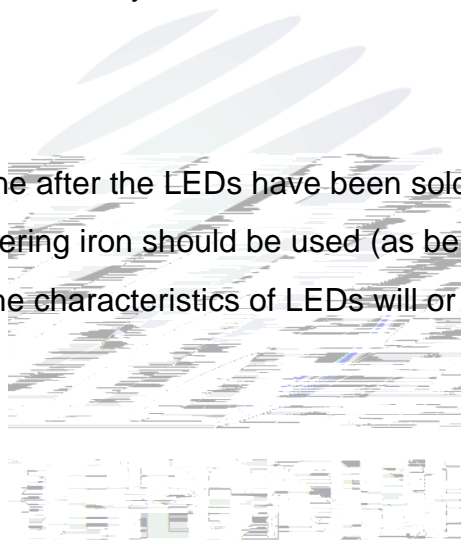
3.1.1 Soldering Iron

(1) When do soldering by hand, keep the temperature of iron below less 300°C less than 3 seconds.

(2) Soldering by hand should be done only one time.

3.1.2 Repairing

Repairing should not be done after the LEDs have been soldered. When repairing is unavoidable,a double-head soldering iron should be used (as below figure). It should be confirmed in advance whether the characteristics of LEDs will or not be damaged by repairing.



LED

3.1.3 Cautions

(1) Components should not be mounted on warped (non coplanar) portion of PCB. After soldering, do not warp the circuit board.LED

(2) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering. Do not rapidly cool device after soldering.



4. Handling Precautions

4.1 Handling Precautions



(4) Handle the component along the side surface by using forceps or appropriate tools; Do not directly touch or Handle the silicone lens surface, it may damage the internal circuitry.

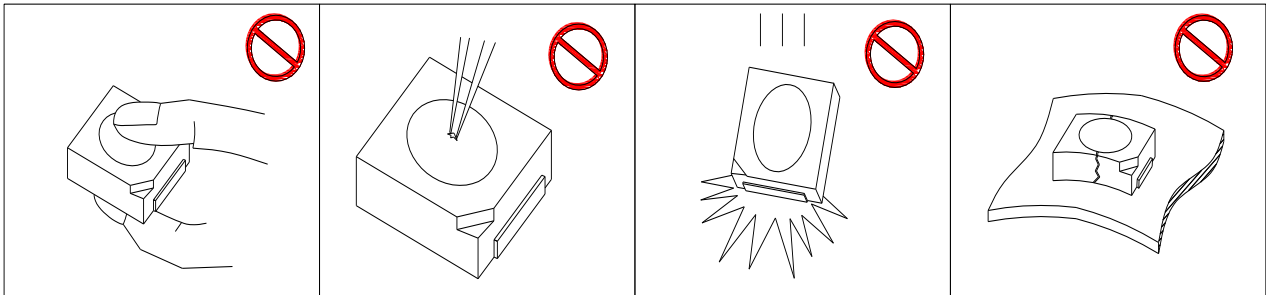
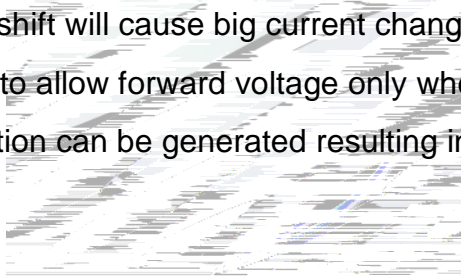


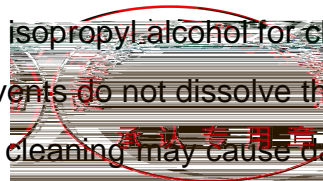
Fig 4-1 Handling Precautions

(5) In designing a circuit, the current through each LED can not exceed the absolute maximum rating specified for each LED. In the mean while, resistors for protection should be applied, otherwise slight voltage shift will cause big current change, burn out may happen. The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.



(6) Thermal Design is paramount importance because heat generation may result in the Characteristics decline, such as brightness decreased, Color change and so on. Please consider the heat generation of the LEDs when making the system design. LED

(7) Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust, requiring special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components. Refond suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the

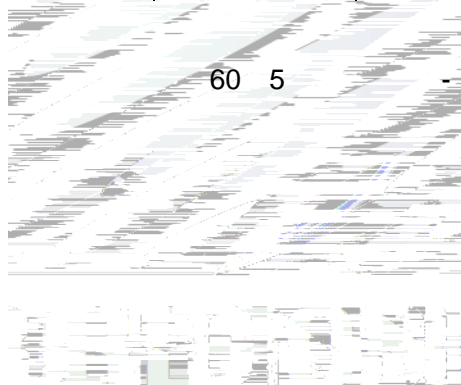


LED.

Table 4-1 Storage

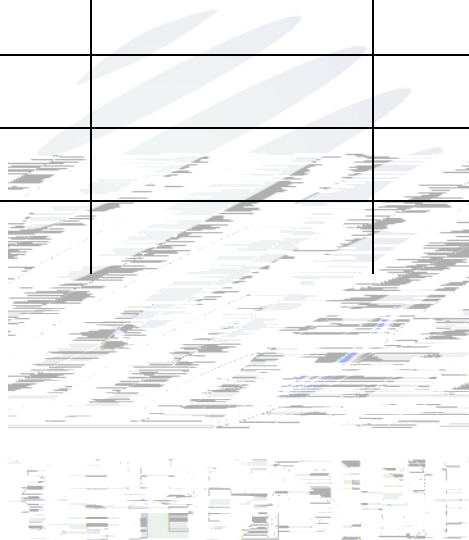
Conditions		Temperature	Humidity	Time
Storage	Before Opening Aluminum Bag	30	75%	Within 1 Year From Date
	After Opening Aluminum Bag	30	60%	24hours 24

Baking



24hours

Date	Revisor	Version	Verifier	Remarks
2016.12.12		E/0		
2017.12.09		E/1		
2021.08.10		E/2		





Declare

This specification is written both in English and in Chinese and the latter is format.

