

SPECIFICATION

REFOND P/N

RF-A3F30-WYS8-A2

D~ 6

Mass Production

Contents

1. Description	
1.1 General Description	
1.2 Features	
1.3 Application	
1.4 Package Dimension	
1.5 Product Parameters	
1.6 Bin Range Of Forward Voltage and Luminous Flux (IF=50mA)	BIN
(IF=50mA)	
1.7 Typical Optical Characteristics Curves	
2. Packaging	
2.1 Packaging Specification	
2.1.1 Carrier Tape Dimension	13
2.1.2 Reel Dimension	13
2.1.3 Label Form Specification	14
2.2 Moisture Resistant Packing	
2.3 Cardboard Box	
2.4 Reliability Test Items And Conditions	
2.5 Criteria For Judging Damage	
SMT Reflow Soldering Instructions SMT	
3.1 SMT Reflow Soldering Instructions SMT	
4. Handling Precautions	
4.1 Handling Precautions	

1. Description

1.1



The Yellow LED, which was fabricated by using a blue chip and the phosphor.

Product Package: 3.00mmX1.40mmX0.52mm.

>76

3.00mmX1.40mmX0.52mm.

1.2 Features

7? 5 BSUJ SYV7? 5

Extremely wide viewing angle.

Suitable for all SMT assembly and solder process. E? F

Available on tape and reel.

Moisture sensitivity level: Level 2. >VWWS

RoHS compliant. RoHS

Qualifications: The product qualification test plan is based on the guidelines of AEC-Q102 Stress Test Qualification for Automotive Grade Discrete Semiconductors

375ŽC# " \$

1.3 Application

Automotive Lighting Interior and Exterior.

1.5 Product Parameters

Table 1-1 Electrical / Optical Characteristics at Ts=25°C

Item	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Forward Voltage	V_F	$I_F=50\text{mA}$	2.7	--	3.2	V
Reverse Current	I_R	$V_R=5\text{V}$	---	---	10	μA
Luminous Flux		$I_F=50\text{mA}$	13.0	--	17.7	lm
Viewing Angle		$I_F=50\text{mA}$	---	120	---	deg
Thermal Resistance (Junction to Solder) ($R_{th\ JS\ real}$	$I_F=50\text{mA}$	---	70	90	/W
	$R_{th\ Jsel}$	$I_F=50\text{mA}$	---	45	60	/W

Table 1-2 Absolute Maximum Ratings at Ts=25°C

Parameter	Symbol	Rating	Units
Power Dissipation	P_D	224	mW
Forward Current	I_F	70	mA
Peak Forward Current	I_{FP}	150	mA
Reverse Voltage	V_R	5	V
Electrostatic Discharge (HBM)	E_{SD}	8000	V
Operating Temperature	T_{OPR}	-40 ~ +110	°C
Storage Temperature	T_{STG}	-40 ~ +110	°C
Junction Temperature	T_J	125	°C

Notes

1. 1/10 Duty cycle, 10ms pulse width. $f = 1000 \text{ Hz}$
2. The above forward voltage measurement allowance tolerance is $\pm 0.1 \text{ V}$. $f = 1000 \text{ Hz}$
3. The above color coordinates measurement allowance tolerance is ± 0.005 . ± 0.005
4. The above luminous flux measurement allowance tolerance $\pm 10\%$. $f = 1000 \text{ Hz}$
5. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
6. All measurements were made under the standardized environment of Refond.



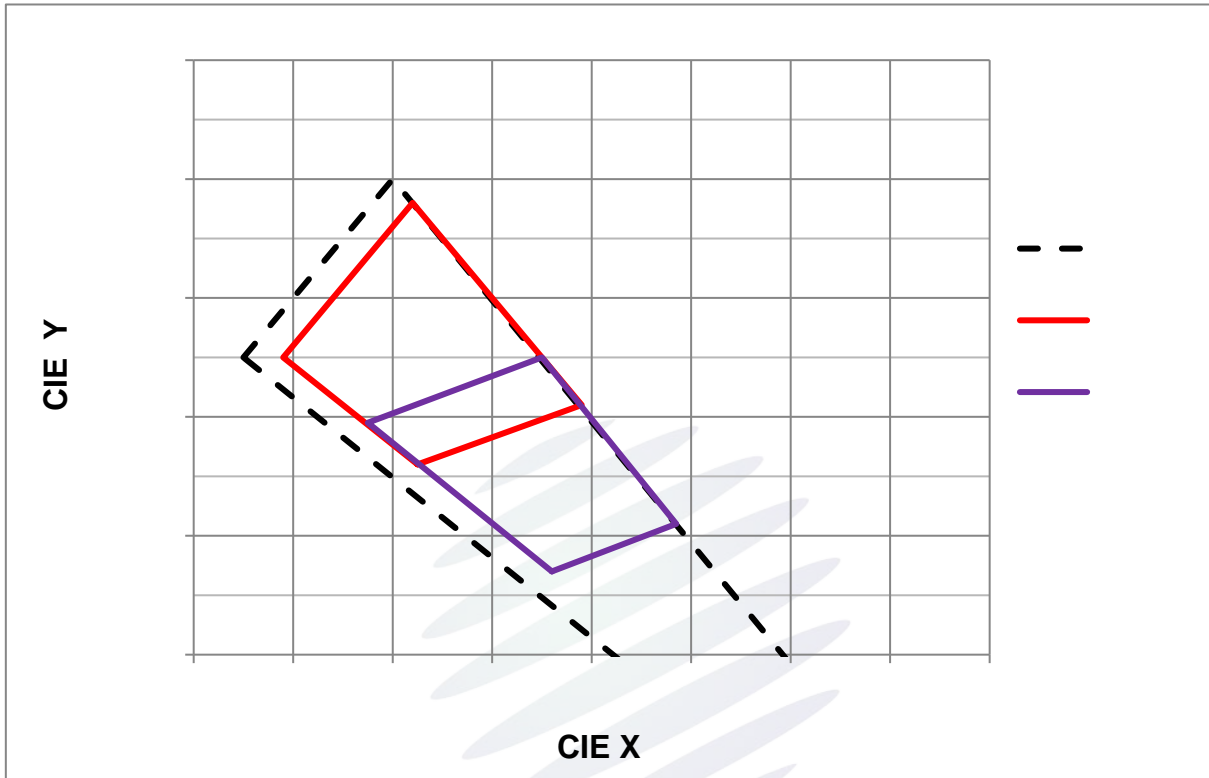


Fig. 1-6 The C.I.E Chromaticity Diagram CIE

Table 1-4

BIN CODE	CIE-X1	CIE-Y1	CIE-X2	CIE-Y2	CIE-X3	CIE-Y3	CIE-X4	CIE-Y4
AM1	0.5625	0.4160	0.5490	0.4250	0.5620	0.4380	0.5790	0.4210
AM2	0.5760	0.4070	0.5575	0.4195	0.5750	0.4250	0.5885	0.4110

1.7 Typical Optical Characteristics Curves

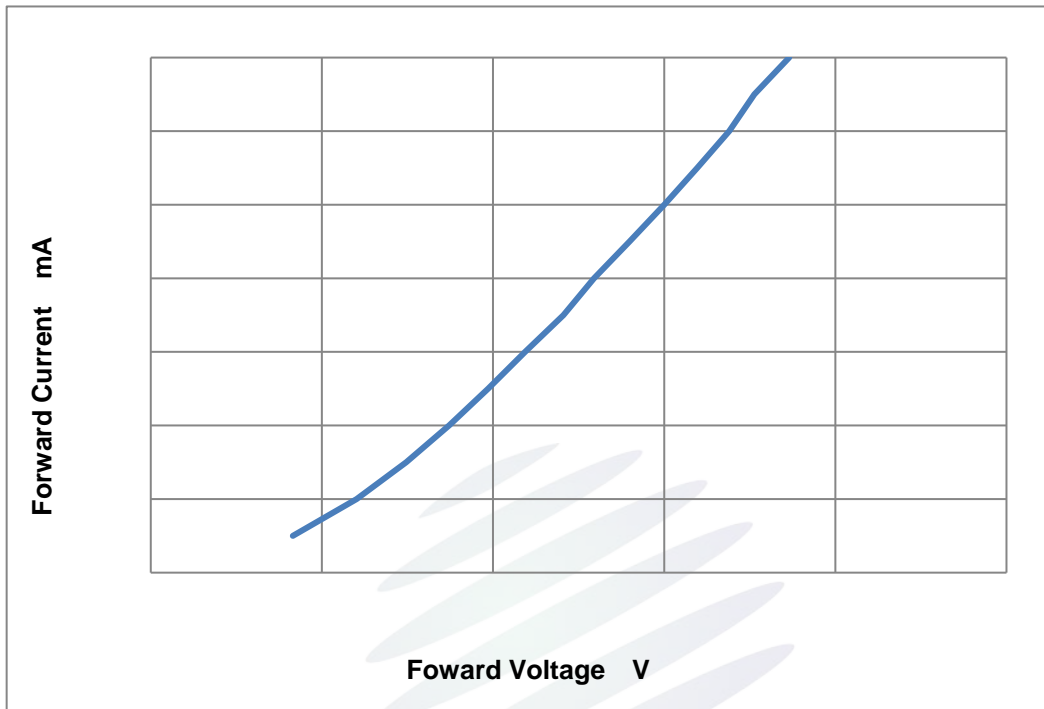


Fig. 1-7 Forward Voltage Vs Forward Current

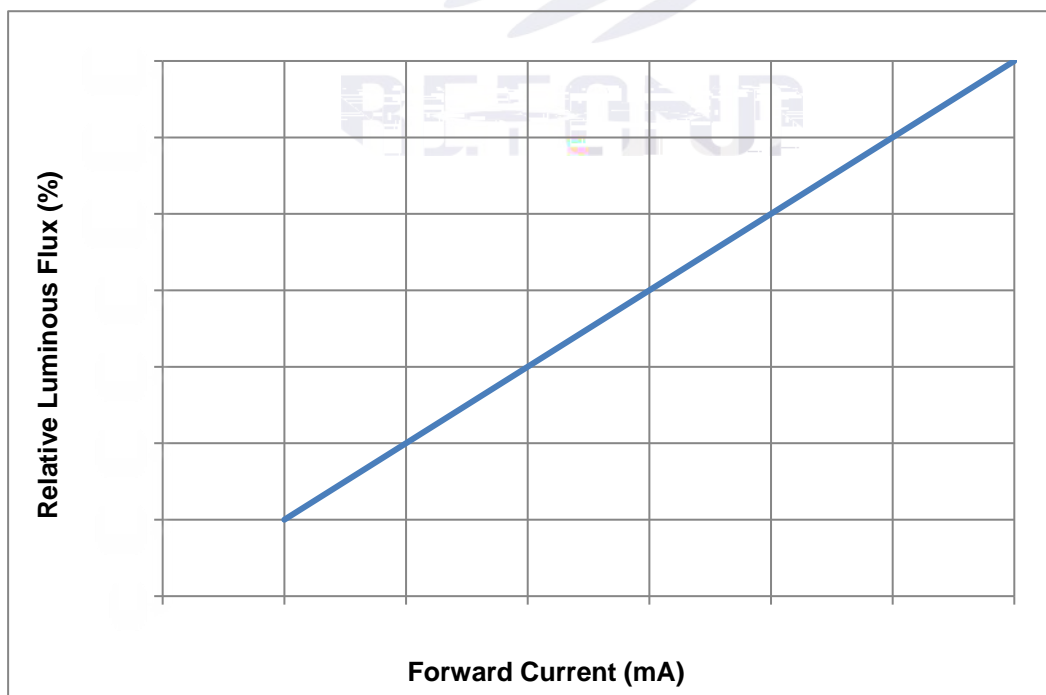


Fig. 1-8 Forward Current Vs Relative Intensity

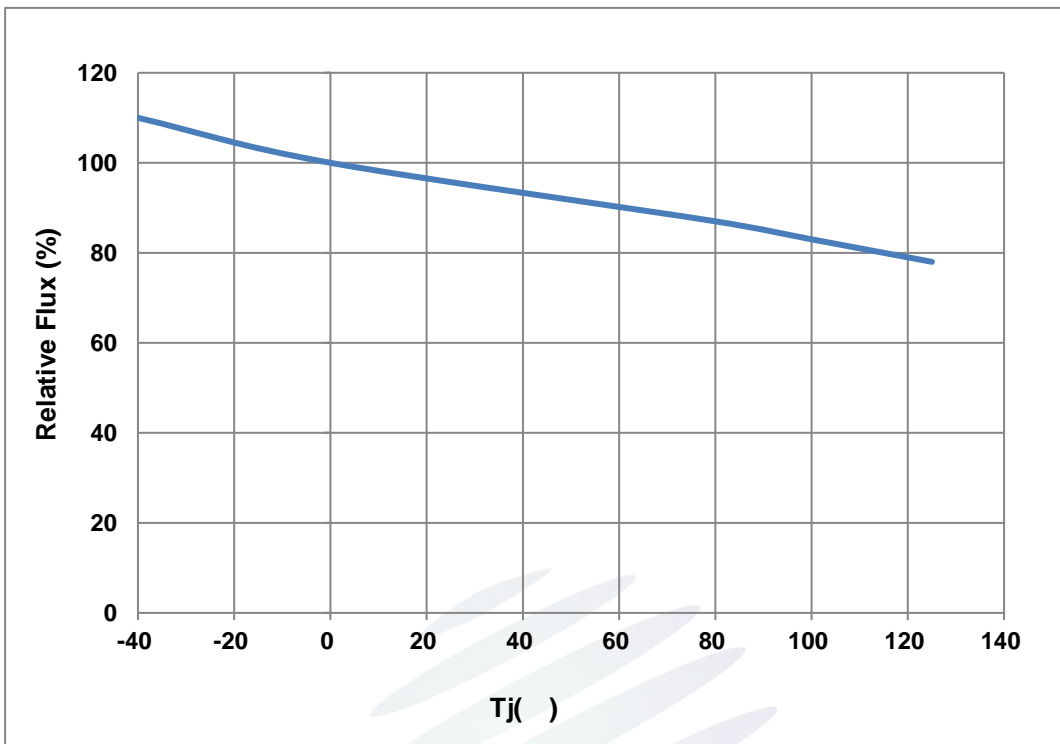


Fig. 1-9 Junction Temperature Vs Relative Flux

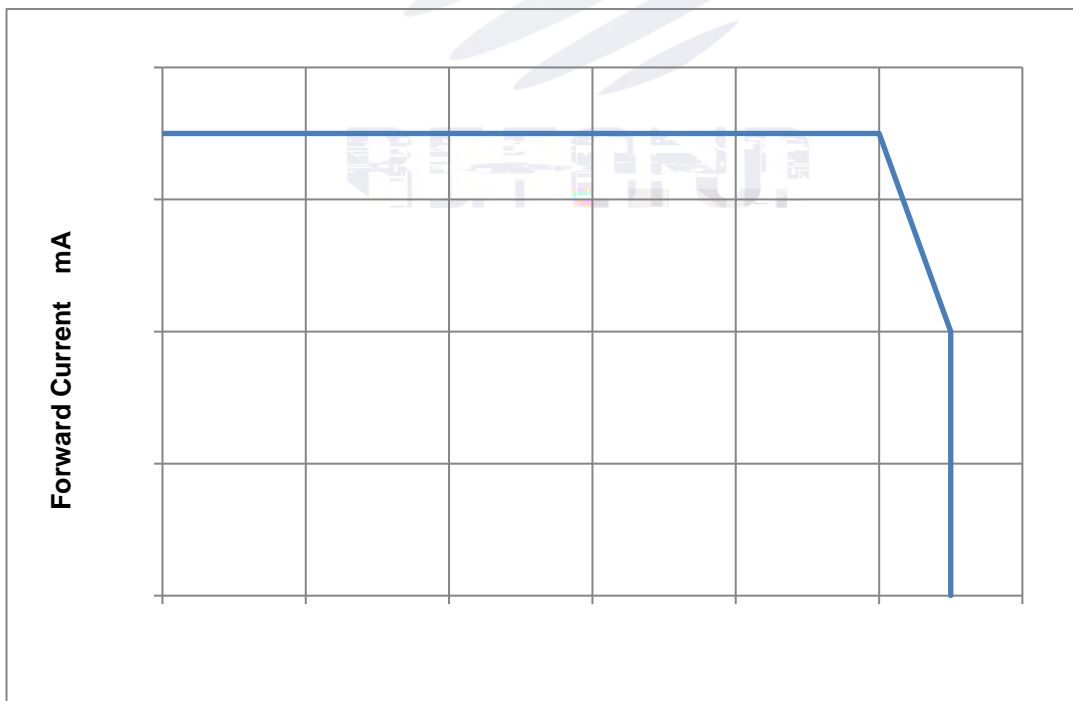


Fig. 1-10 Solder Temperature Vs Forward Current

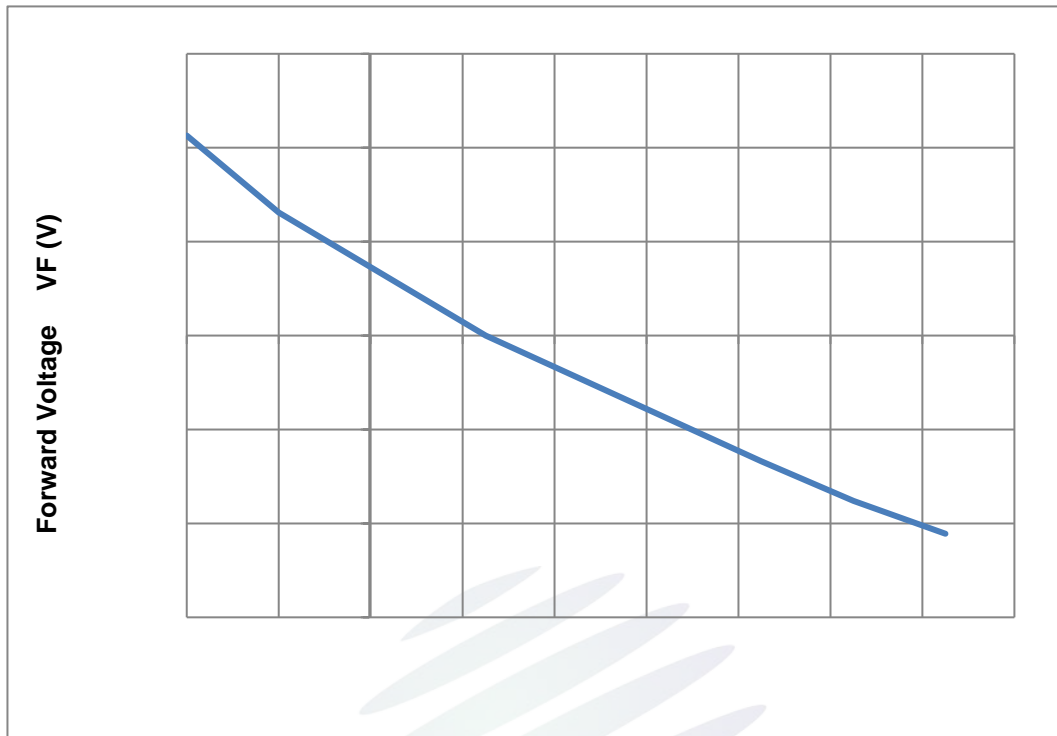


Fig. 1-11 Voltage shift Vs Junction Temperature

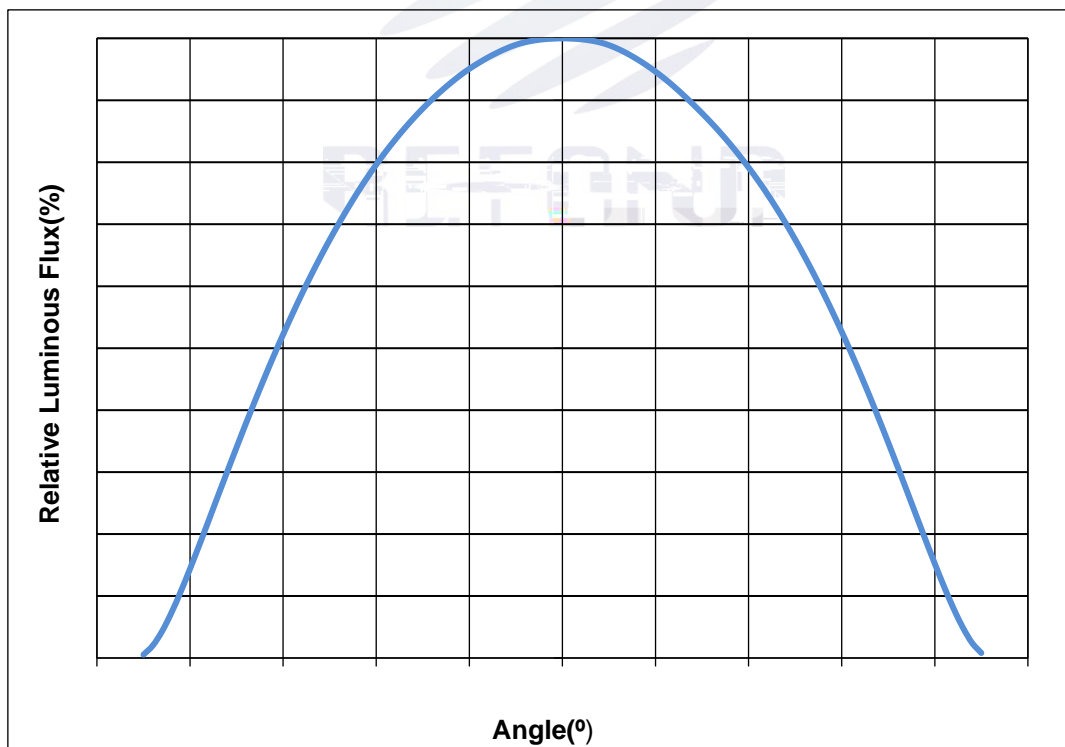


Fig. 1-12 Radiation diagram

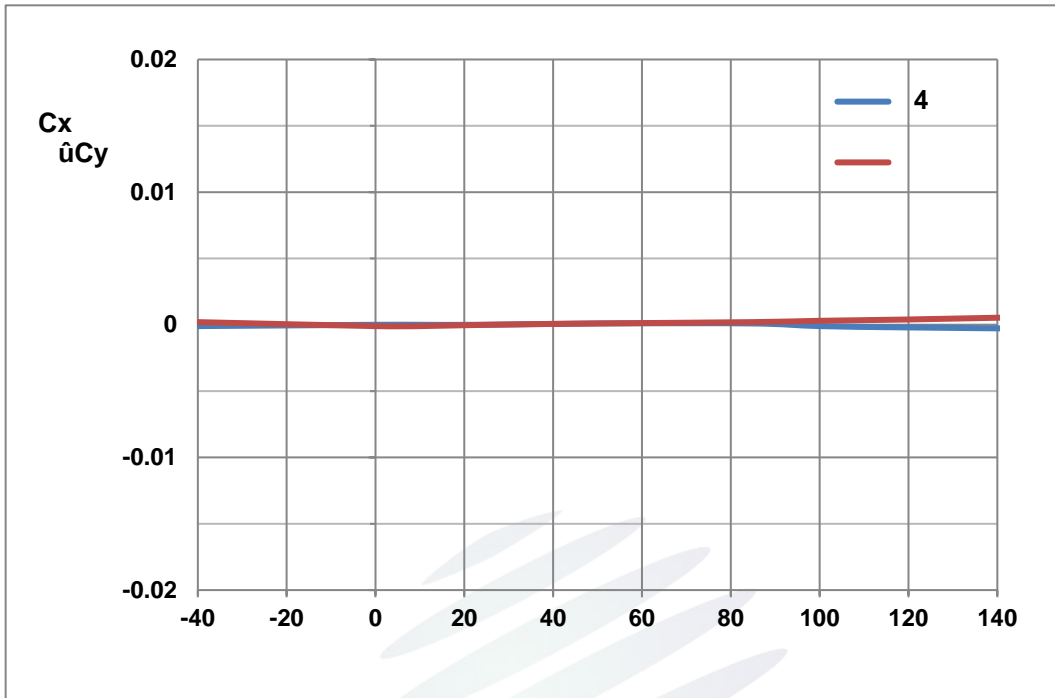


Fig. 1-13 Color coordinate shiftVs Junction Temperature

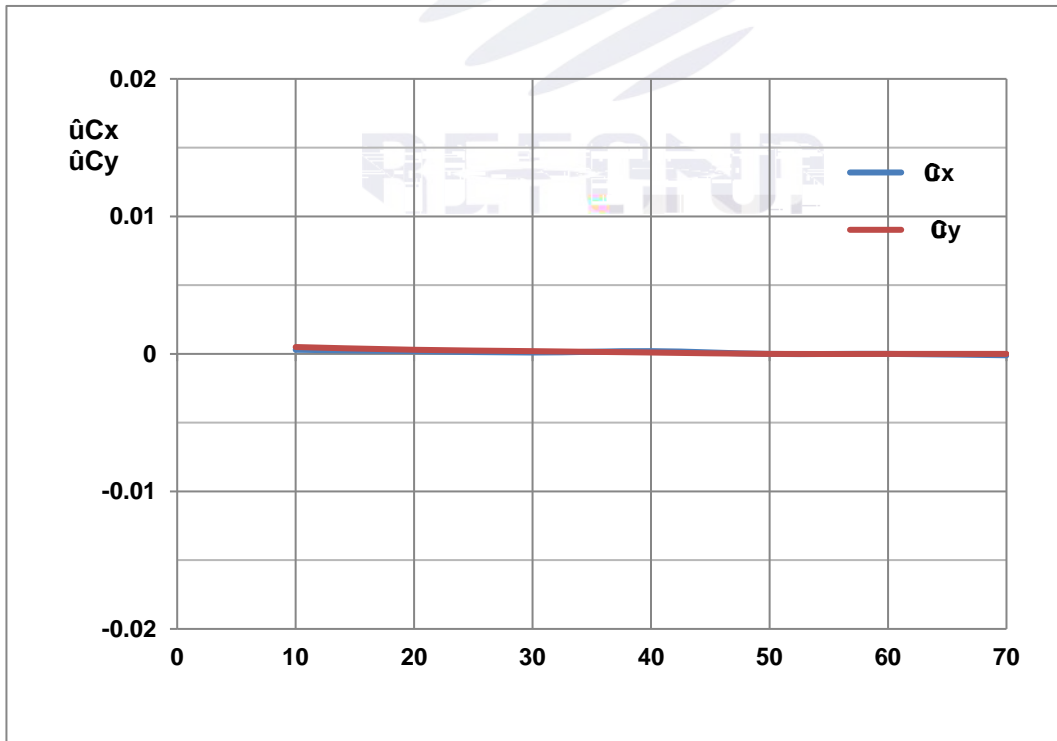


Fig. 1-14 Color coordinate shiftVs Forward Current

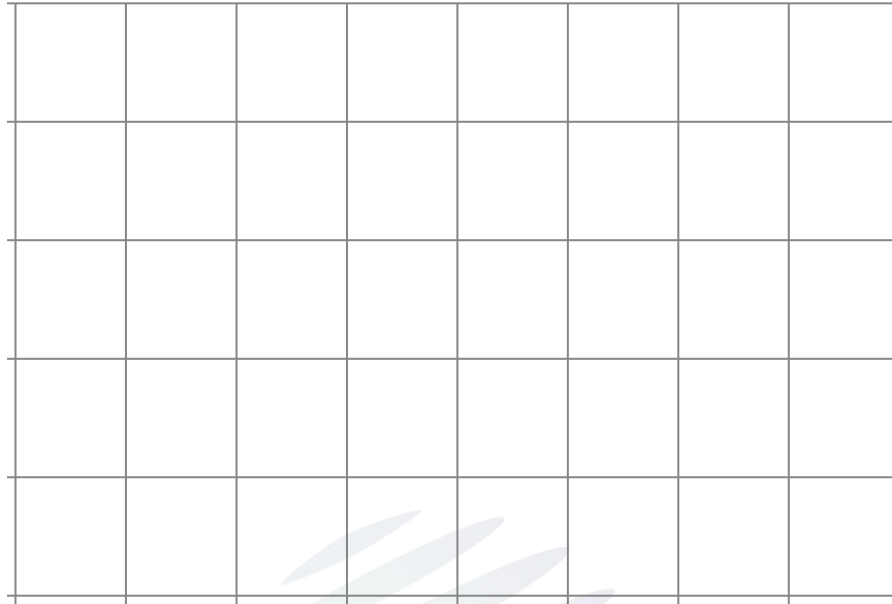


Fig. 1-15 Spectrum Distribution



2. Packaging

2.1 Packaging Specification

Package:2000pcs/reel.

2.1.1 Carrier Tape Dimension

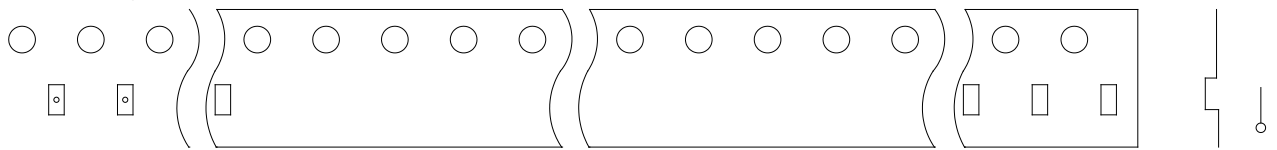


Fig.2-1 Carrier Tape Dimension

2.1.2 Reel Dimension

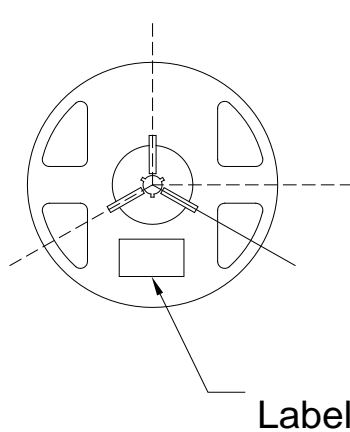


Fig.2-2 Reel Dimension

FST W\$# Reel Dimension

A	8.0f 0.1mm
B	178f 1mm
C	60f 1mm
D	13.0f 0.5mm

Notes

The tolerances unless mentioned $\pm 0.1\text{mm}$. Unit : mm

f "ž#

2.1.3 Label Form Specification

ST WSS Specification

PART NO.	Part Number
SPEC NO.	Spec Number
LOT NO.	Lot Number
BIN CODE	Bin Code
	Luminous flux
XY	Chromaticity Bin
V _F	Forward Voltage
WLD	Wavelength
QTY	Packing Quantity
DATE	Made Date

Fig. 2-3 Label Form Specification

2.2 Moisture Resistant Packing



Moisture Resistant Packing

2.3 Cardboard Box

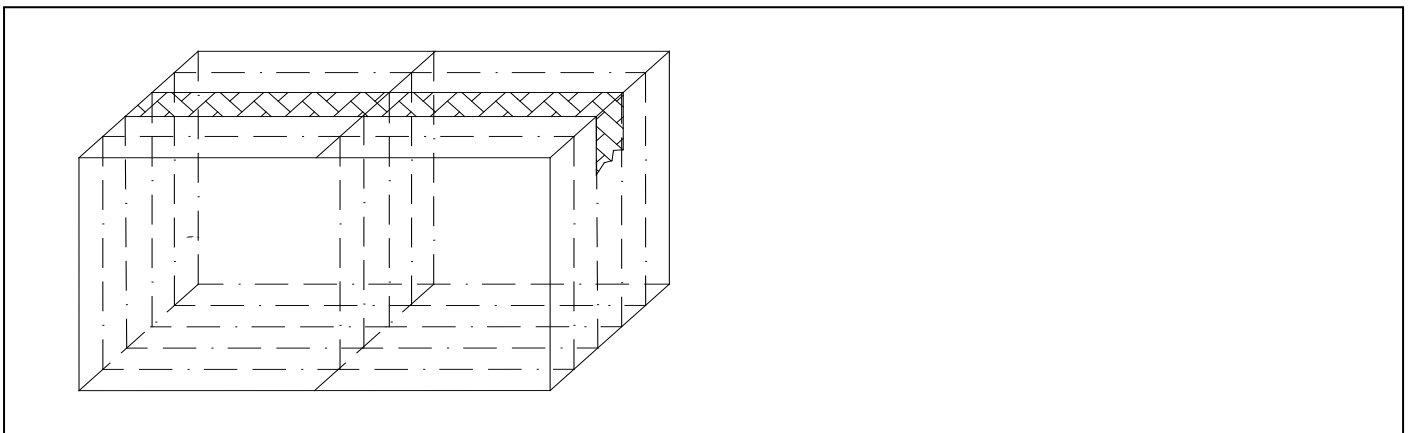


Fig.2-5 Cardboard Box

2.4 Reliability Test Items And Conditions

Table 2-3 Reliability Test Items And Conditions

Test Items	Ref.Standard	Test Condition	Time	Quantity	Ac/Re /
Reflow	JESD22-B106	Temp:260 max T=10 sec	2times	20pcs.	0/1
MSL2 2	JESD22-A113	85 / 60%RH	168 hrs.	20pcs.	0/1
Thermal Shock	JEITAED-4701 300307	-40 15min 10s 125 15min	1000 cycle	20pcs.	0/1
Life Test	JESD22-A108	Ta=105 IF=50mA	1000hrs.	20pcs.	0/1
High Temperature High Humidity Life Test	JESD22-A101	85 / 85%RH IF=50mA	1000hrs.	20pcs.	0/1

2.5 Criteria For Judging Damage

Table 2-4Criteria For Judging Damage

Test Items	Symbol	Test Condition	Criteria For Judgement	
			Min.	Max.
Forward Voltage	V_F	$I_F=50mA$	-	U.S.L*)x1.1
Reverse Current	I_R	$V_R = 5V$	-	U.S.L*)x2.0
Luminous Flux		$I_F=50mA$	L.S.L*)x0.7	-

Notes

- 1.U.S.L: Upper standard level L.S.L: Lower standard level
- 2.The above reliability tests is based on the verification of a single/strip LED of Refond's existing experimental platform,the reliability experiment was taken under good heat dissipation conditions.

REFOND SMT Reflow Soldering Instructions SMT

3.1 SMT Reflow Soldering Instructions SMT

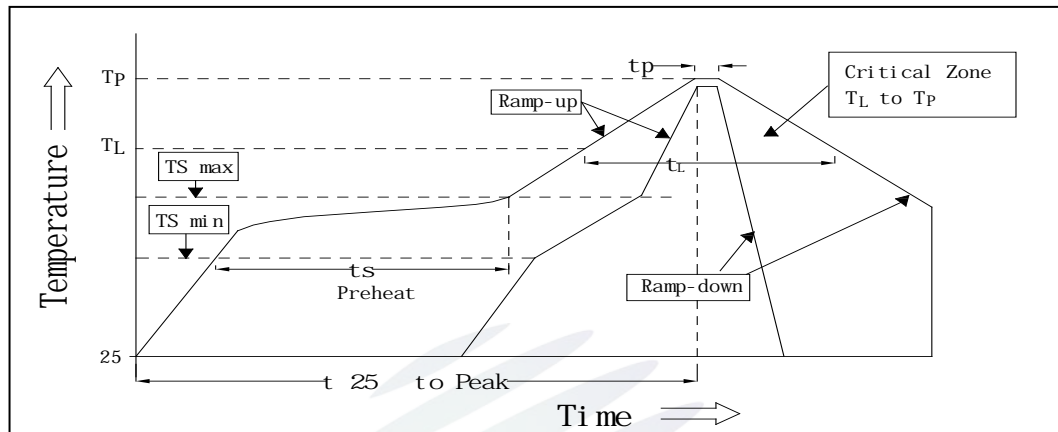


Fig.3-1 SMT Reflow Soldering Instructions SMT

Table 3-1 Reflow parameters

Average temperature rise speed	$T_{smax} - T_P$	3 °C/ s Max 3 °C/ s
Preheating: minimum temperature	(T_{smin})	150 °C
Preheating: Max temperature	(T_{smax})	200 °C
Preheating: Time	$T_{smin} - T_{smax}$	60 - 120 s 60s-120s
Time limited to maintain high temperature: the temperature	(T_L)	217 °C
Time limited to maintain high temperature: The Time	(t_L)	60 s Max 60s
Peak /Classification of temperature:	(T_P)	260 °C
Time limit classification of peak temperature time	t_p	10 s Max 10s
(T_P) 5 °C actual peak temperature (T_P)	Hold time within 5 °C with the	30 s Max 30s
Cooling speed		6 °C/ s Max 6 °C/ s
25 °C	Needed time from 25 °C to T_P	8 minutes Max 8 minutes

Notes

(1)Reflow soldering should not be done more than twice. If more than 24 hours between the two solderings ,LED will be damaged.

\$& >76

(2)When soldering , do not put stress on the LEDs during heating.

3.1.1 Repairing

Repairing should not be done after the LEDs have been soldered. When repairing is unavoidable,a double-head soldering iron should be used (as below figure). It should be confirmed in advance whether the characteristics of LEDs will or not be damaged by repairing.

LED

>76

3.1.2 Cautions

(1) The encapsulated material of the LEDs is silicone. Therefore the LEDs have a soft surface on the top of package. The pressure to the top surface will be impacted on the reliability of the LEDs. Precautions should be taken to avoid the strong pressure on the encapsulated part. So when use the picking up nozzle, the pressure on the silicone resin should be proper. LED

>76

(2) Components should not be mounted on warped (non coplanar) portion of PCB. After soldering, do not warp the circuit board.LED

B54

(3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering. Do not rapidly cool device after soldering.

4. Handling Precautions

4.1 Handling Precautions

(1) LED operating environment and sulfur element composition cannot be over 100PPM in the LED mating usage material. This is provided for informational purposes only and is not a warranty or endorsement. LED

>76

" " BB? ž

(2) In order to prevent ex-ternal material from getting into the inside of LED, which may cause the malfunction of LED, the single content of Bromine element is required to be less than 900PPM, the single content of Chlorine element is required to be less than 900PPM, the total content of Bromine element and Chlorine element in the external materials of the application products is required to be less than 1500PPM. This is provided for informational purposes only and is not a warranty or endorsement.

>76

>76

+ " " BB?

+ " " BB?

" " BB? ž

(3) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues. Refond advises against the use of any chemicals or materials that have been found or are suspected to have an adverse affect on device performance or reliability. To verify compatibility, Refond recommends that all chemicals and materials be tested in the specific application and environment for which they are intended to be used. Attaching LEDs, do not use adhesives that outgas organic vapor.

>76

>76

>76

>76

(4) Handle the component along the side surface by using forceps or appropriate tools; Do not directly touch or Handle the silicone lens surface, it may damage the internal circuitry.

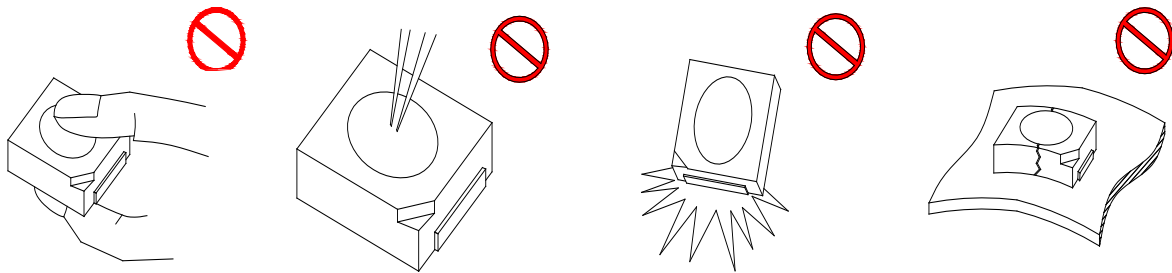


Fig 4-1Cautions

(5) In designing a circuit, the current through each LED can not exceed the absolute maximum rating specified for each LED. In the mean while, resistors for protection should be applied, other wise slight voltage shift will cause big current change, burn out may happen. The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.

>76

>76

(6) Thermal Design is paramount importance because heat generation may result in the Characteristics decline, such as brightness decreased, Color change and so on. Please consider the heat generation of the LEDs when making the system design. LED

>76

(7) Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust, requiring special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components. Refond suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.

>76



Declare

This specification is written both in English and in Chinese and the latter is formal.